

FOR IMMEDIATE RELEASE

V•I Chip Data Center Architecture White Paper and Exhibit at Digital Power Forum

Andover, MA, September 6, 2007...V•I Chip Inc., a subsidiary of Vicor Corporation (NASDAQ:VICR), announced that it will present and exhibit at Darnell's Digital Power Forum to be held in San Francisco on September 10 - 12.

This three-day international conference has become an important forum for decision makers who are interested in learning about and contributing to the latest advancements in digital control and power conversion.

Paul Yeaman, V•I Chip Principal Engineer, will deliver a paper entitled "Data Center Power Delivery Architectures: Size and Total Cost of Ownership" as part of the Mission Critical Facilities track. Four power architectures will be analyzed and benchmarked in terms of overall efficiency, power conversion footprint, and annual electrical operating costs. This is increasingly important in light of the technical challenges relating to sub-1V microprocessors and the continued increases in the cost of electricity.

The V•I Chip exhibit, Booths #100/102, will present the latest information on the V•I Chip power solutions and feature an active product demonstration highlighting the speed, power density, flexibility, and efficiency of these innovative power components.

The white paper will be presented on Tuesday, 11 September 2007 from 3:15 p.m. Exhibit hours are Monday, 10 September, 5:00p.m. to 7:00 p.m. and on Tuesday, 11 September, 12:45 p.m. to 3:00 p.m.

Vicor Corporation designs, develops, manufactures, and markets modular power components and complete power systems used primarily by original equipment manufacturers (OEMs) in the communications, data processing, industrial control, test equipment, medical, and defense electronics markets.

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